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WHAT IS CLAIMED IS :

1. In a communication system including a base transceiver station and a base station controller controlling the base transceiver station, an apparatus for monitoring asynchronous transfer mode cells in a communication system comprising:

a receive interface part established in the base station controller, the receive interface part recording a cell to be monitored in a storage part by checking latched VPI/VCI of asynchronous transfer mode cells received from the base transceiver station by the base station controller, the receive interface part counting the number of error occurrence by checking header errors of the cells; and

a transmission interface part established in the base station controller, the transmission interface part transferring a test cell produced for checking a cell transmission time between the base transceiver station and the base station controller to the base transceiver station.

- 2. The apparatus of claim 1, the receive interface part includes:
- a first storage part storing the cell to be monitored temporarily;
- a second storage part outputting a data corresponding to the latched VPI/VCI of the cell;

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- a third storage part outputting a VPI index value of an address designated by the data outputted from the second storage part; and
- a fourth storage part storing the number of error occurrence of the cell at the other address designated by the VPI index value.
- 3. The apparatus of claim 2, wherein the third and fourth storage parts are RAMs having dual ports.
- 4. The apparatus of claim 2, wherein the first storage part outputting a previously-stored cell in advance.
- 5. The apparatus of claim 2, wherein the second storage part outputs a data when there exists a matched VPI/VCI by comparing the VPI/VCI of the cell to a reference value.
- 6. The apparatus of claim 2, wherein the test cell is stored in the first storage part by being transferred from the base transceiver station to the base station controller.
- 7. The apparatus of claim 1, further comprising:

 a GPS receiver receiving a time information packet from GPS, the GPS receiver providing the receive and transmission parts with the time information packet; and
 - a controller producing a test cell including a time information

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provided by the GPS receiver or checking a cell transmission delay time by comparing transceiving time of a cell loop-backed from the base transceiver station.

- 8. The apparatus of claim 7, wherein the receive and transmission parts are provided with the time information with which the respective interface parts are provided when the test cell is transceived from/to the base station controller to/from the base transceiver station.
 - 9. The apparatus of claim 1, wherein the VPI/VCI is latched with hardware.
 - 10. The apparatus of claim 1, wherein the base transceiver station and the base station controller are interfaced by E1.
 - 11. The apparatus of claim 1, wherein the transmission and receive interface parts are established in a function part multiplexing /demultiplexing a cell inputted from an asynchronous transfer mode cell switch included in the base transceiver station or the base station controller.